



**FOR IMMEDIATE RELEASE**

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**CPU Tech to Demonstrate SystemLab PS™ Platform Simulator Running Large Scale Electronic Modeling and Simulation at SC07 in Reno, Nevada**

**PLEASANTON, Calif. – October 29, 2007** – CPU Technology, Inc.® ([www.cputech.com](http://www.cputech.com)) has announced plans to demonstrate its advanced System-on-a-Chip (SoC) technology, with demos of the SystemLab PS™ Platform Simulator, as an exhibitor at SC07, the International Conference for High Performance Computing, Networking, Storage, and Analysis. SC07 is expected to draw 10,000 attendees for the annual conference and exhibition being held at the Reno Sparks Convention Center, Reno, Nevada, November 10-16, 2007. CPU Tech will be demonstrating the SystemLab Platform Simulator in booth number 150 in the SC07 exhibition hall which is open November 13, 14 and 15.

CPU Tech's SystemLab PS Platform Simulation solution was announced in May, 2007, after ten years of research and development, and represents breakthrough technology in modeling and simulation. The SystemLab PS product line from CPU Tech is a complete high productivity solution that includes hardware, enterprise software, virtual model development, custom library elements, training and support. SystemLab PS is the industry's first large scale, high fidelity platform simulator that runs actual software providing extraordinary visibility and control of every wire, every memory, every processor and every line of code to collaborative teams in real-time.

The company also develops the Acalis® family of Field Programmable MultiCore chips (FPMCs). Acalis FPMCs facilitate the migration of microprocessor based systems to multi-core computing, while uniquely preserving the investment in existing software. Acalis products are applicable to a broad range of general purpose and embedded computing systems.

The company recently announced collaborative development agreements with BAE Systems and with EADS North America Defense Test & Services. To see recent CPU Tech news releases, go to: <http://www.cputech.com/press-releases.htm>

Where: Booth #150: SC07 – at the Reno Sparks Convention Center, Reno, Nevada  
<http://sc07.supercomputing.org/>

When: November 13, 14 and 15

What: Demonstrations of the CPU Tech SystemLab PS™ Platform Simulation solution running high-fidelity, real-time simulations with full system visibility and control

**About CPU Technology, Inc.**

CPU Tech leads the industry in large scale electronic modeling and simulation. CPU Tech's Compatible System-on-a-Chip (SoC) solutions include the company's real time, cycle accurate platform simulator, SystemLab PS™, the Acalis™ family of Field Programmable Multicore Chips (FPMCs), and the workhorse of the SystemLab™ modeling environment, the SuperQ™ X3 supercomputer. The company's products and services directly address the challenges associated with the transformation of microprocessor-based electronic systems to the SoC / MultiCore era. CPU Tech is a privately held company founded in 1989 with headquarters in Pleasanton, CA, and business development offices in Reston, VA. Visit [www.cputech.com](http://www.cputech.com) for additional information.