



## NEWS RELEASE

### FOR IMMEDIATE RELEASE

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### **CPU TECH TO DEMONSTRATE SYSTEMLAB PS™ REAL-TIME SIMULATION AND EXHIBIT ACALIS® SOC-BASED SECURE AND COMPATIBLE MODERNIZATION AT AUSA**

**PLEASANTON, Calif. – September 30<sup>th</sup>, 2008** – CPU Technology, Inc.® ([www.cputech.com](http://www.cputech.com)) announced today it will participate at the AUSA Annual Meeting and Exposition, October 6-8<sup>th</sup>, 2008 at the Walter E. Washington Convention Center in Washington, DC, in Business to Business Suite K. At this convention, the company will illustrate the simulation of complete platform electronics, at real-time speeds, of a combat vehicle with its SystemLab PS™. The models will be running actual vehicle software with visibility into every circuit, every SRU, every LRU and every line of software. CPU Tech® is the only company with a real-time, high fidelity, high capacity platform simulator that operates on this level.

CPU Tech will also highlight its tamper-resistant and SWAP reducing System-on-Chip (SoC) modernization process. With their SystemLab suite of tools and their trusted, secure and anti-tamper protected Acalis® family of field programmable multi-cores, CPU Tech is able to significantly reduce the electronics of an entire LRU while compatibly maintaining all of the existing software of that LRU. This effort increases reliability and performance while reducing costs and obsolescence.



“SystemLab PS can simulate highly accurate models allowing our customers to accelerate the time to field for high-quality vehicle electronic systems. Not only have we simulated our customer’s complete vehicle electronics in real-time, but we have enabled them to find and correct faults, in a few hours, that would otherwise have taken weeks of months without the accuracy and visibility of SystemLab PS,” said Ed King, Founder and Corporate Fellow at CPU Tech.

#### **About CPU Technology, Inc.**

CPU Tech leads the industry in large scale electronic modeling and simulation. CPU Tech’s Compatible System-on-a-Chip (SoC) solutions include the company’s real time, high fidelity platform simulator, SystemLab PS™ and the Acalis® family of Field Programmable MultiCore Chips (FPMCs). The company’s products and services directly address the challenges associated with the transformation of microprocessor-based electronic systems to the SoC / MultiCore era. CPU Tech is a privately held company founded in 1989 with headquarters in Pleasanton, CA, and business development offices in Reston, VA. Visit [www.cputech.com](http://www.cputech.com) for additional information.