



NEWS RELEASE

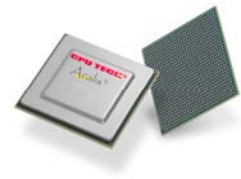
FOR IMMEDIATE RELEASE

For more information, contact:
Robert Beanland
CPU Tech
r.beanland@cputech.com
(925) 224-9920

CPU Tech to Exhibit Trusted and Secure Processing at ESC Boston

PLEASANTON, Calif. – October 23rd, 2008 – CPU Technology, Inc. (CPU Tech) announced today it will participate at the Embedded Systems Conference, October 28-29th, at the Hynes Convention Center, Boston, MA, in booth # 320. The company will exhibit its Acalis CPU872 Field Programmable MultiCore System-on-Chip (SoC), the first SoC to directly address the new Department of Defense S.3001 requirement for anti-tamper technology (AT) for defense systems. The CPU872 allows systems designers to add supply chain and runtime protection to their mission critical systems. The device boasts dual, high performance PowerPC[®] 440 and FPU cores, embedded DRAM, and specialized compute engines to accelerate computation of complex algorithms, communications, and synchronization between processors while minimizing power dissipation. The CPU872 brings together the programming environment of the PowerPC ecosystem with the defenses necessary to meet new government requirements for deterring reverse engineering and exploitation of IP.

“Our Acalis family of SoC devices are a performance / watt leader and have been deployed in critical defense systems,” said Ed King, founder and Chairman of the Board. “Our latest device is the first COTS (commercial, off the shelf) processor with tamper protection. This technology enables organizations to meet new government anti-tamper regulations while providing fundamental protection against major security issues resulting from tampering within their supply chain.”



The CPU872 is an industrial grade device with a guaranteed minimum ten year life span. Evaluation kits, including evaluation boards and advanced SystemLab[®] development tools, will be available in Q1, 2009. To learn more call (925) 224-9920 or contact Acalis@cputech.com.

About CPU Technology, Inc.

CPU Tech leads the industry in large scale electronic modeling and simulation. CPU Tech's Compatible System-on-a-Chip (SoC) solutions include the company's real time, high fidelity platform simulator, SystemLab PS[™], and the Acalis[®] family of Field Programmable MultiCore Chips (FPMCs). CPU Tech is a privately held company founded in 1989 with headquarters in Pleasanton, CA, and a regional office in Reston, VA. Visit www.cputech.com for additional information.

Acalis[®] and SystemLab[®] are registered trademarks of CPU Technology, Inc. PowerPC[®] is a registered trademark of IBM Corporation.

###